Hall Chart

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In re Patent Application of Kamesh V. Gadepally METHOD FOR MANUFACTURING AN INTEGRATED CIRCUIT STRUCTURE ... Express Mail Label No. EL 727769768 US Attorney Docket.: NSC1-G0610 [P04402 P01] Sheet 1 of 3



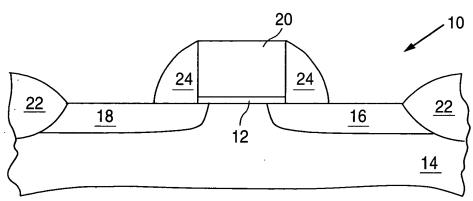


FIG. 1 (PRIOR ART)

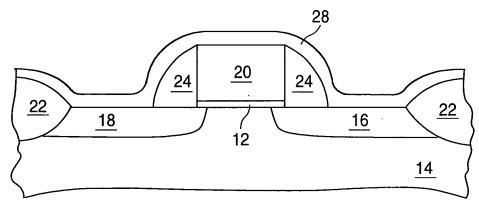


FIG. 2 14 (PRIOR ART)

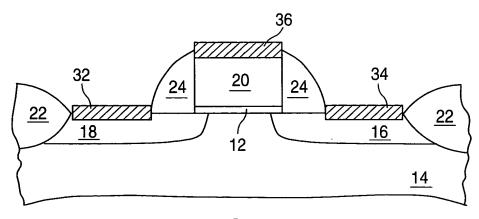


FIG. 3 (PRIOR ART)

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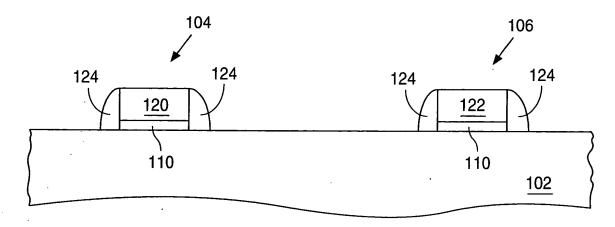


FIG. 4

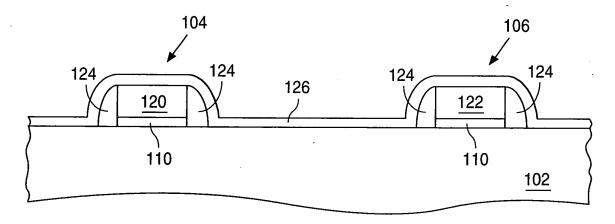


FIG. 5

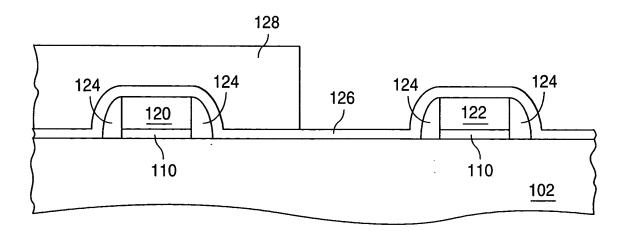


FIG. 6

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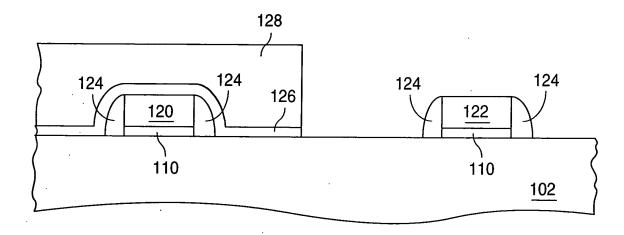


FIG. 7

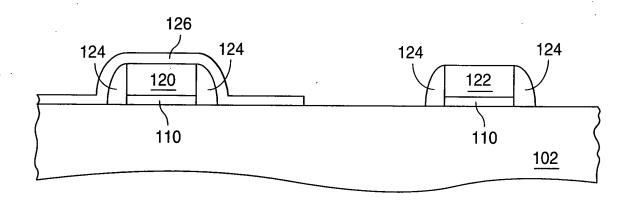


FIG. 8

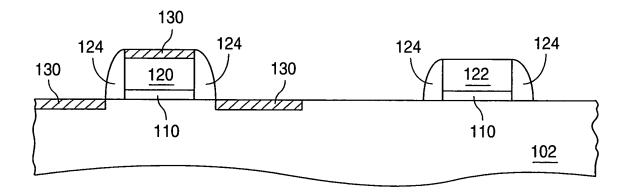


FIG. 9